Docket No.: 8733.915.00

REMARKS

At the outset, the Examiner is thanked for the thorough review and consideration of the pending application. The Final Office Action dated February 11, 2008, has been received and its contents carefully reviewed.

Claims 1, 6, 8, 14, 21, 22 and 24 are currently amended and claims 10, 20 and 25 are deleted. Claims 1-9, 11-19, 21-24, and 26-30 are currently pending. Reexamination and reconsideration of the pending claims is respectfully requested.

In the Office Action, claims 1-3, 7-10, 13-17, 23-24 and 26-27 are rejected under 35 U.S.C. §102(e) as being anticipated by Zhang et al., US 6,847,422 (hereinafter "Zhang"), and claims 4-5, 11-12, 18-19 and 28-29 are rejected under 35 U.S.C. §103(a) as being unpatentable over Zhang in view of Kitakado et al., US 6,461,899.

The rejection of claims 1-7 is respectfully traversed and reconsideration is requested. Claims 1-7 are allowable over the cited reference in that each of these claims recite a combination of elements including, for example, "a second gate redundancy line internal to the display formed on the passivation layer, and connected electrically with the first gate redundancy line through a second gate contact hole." None of the cited references teaches or suggests at least these features of the claimed invention.

Accordingly, claim 1 and claims 2-7, which depend either directly or indirectly on claim 1, are allowable over the cited references.

The rejection of claims 8-13 is respectfully traversed and reconsideration is requested. Claims 8-13 are allowable over the cited reference in that each of these claims recite a combination of elements including, for example, "a gate redundancy line ... formed of the same material as the pixel electrode, wherein the gate contact hole is formed passing through the interlayer insulating layer and passivation layer."

Zhang discloses that wiring pattern 10b (corresponding to gate redundancy line of the present invention) is electrically connected to gate electrode 5b through first interlayer insulating film 9. Thus, Zhang fails to disclose at least these features of the claimed invention. In addition,

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Zhang fails to disclose wiring pattern 10b formed the same material as pixel electrode 19c. None of the cited references teaches or suggests at least these features of the claimed invention.

Accordingly, claim 8 and claims 9 and 11-13, which depend either directly or indirectly on claim 8, are allowable over the cited references.

The rejection of claims 14-23 is respectfully traversed and reconsideration is requested. Claims 14-23 are allowable over the cited reference in that each of these claims recite a combination of elements including, for example, forming a second gate redundancy line internal to the display on the passivation layer connected electrically with the first gate redundancy line through a second gate contact hole." None of the cited references teaches or suggests at least these features of the claimed invention.

---Accordingly, claim 14-and claims 15-19 and 21-23, which depend either directly or indirectly on claim 14, are allowable over the cited references.

The rejection of claims 24-30 is respectfully traversed and reconsideration is requested. Claims 24-30 are allowable over the cited reference in that each of these claims recite a combination of elements including, for example, wherein the gate redundancy line is formed with the same process and the same material as the pixel electrode" None of the cited references teaches or suggests at least these features of the claimed invention.

Accordingly, claim 24 and claims 26-30, which depend either directly or indirectly on claim 24, are allowable over the cited references.

Applicants believe the foregoing remarks place the application in condition for allowance and early, favorable action is respectfully solicited.

If for any reason the Examiner finds the application other than in condition for allowance, the Examiner is requested to call the undersigned attorney at (202) 496-7500 to discuss the steps necessary for placing the application in condition for allowance. All correspondence should continue to be sent to the below-listed address.

If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. § 1.136, and any additional fees required under

37 C.F.R. § 1.136 for any necessary extension of time, or any other fees required to complete the filing of this response, may be charged to Deposit Account No. 50-0911. Please credit any overpayment to deposit Account No. 50-0911. A duplicate copy of this sheet is enclosed.

Dated:

Respectfully submitted,

Eric J. Nuss

Registration No.: 40,106

McKENNA LONG & ALDRIDGE LLP

1900 K Street, N.W. Washington, DC 20006

(202) 496-7500

Attorneys for Applicant